

501.41263X00

#3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): NAKABAYASHI, et al

Serial No.: 10/091,489

Filed: March 7, 2002

For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Attention: APPLICATION DIVISION

REPLY TO OFFICE LETTERCommissioner for Patents
Washington, D.C. 20231

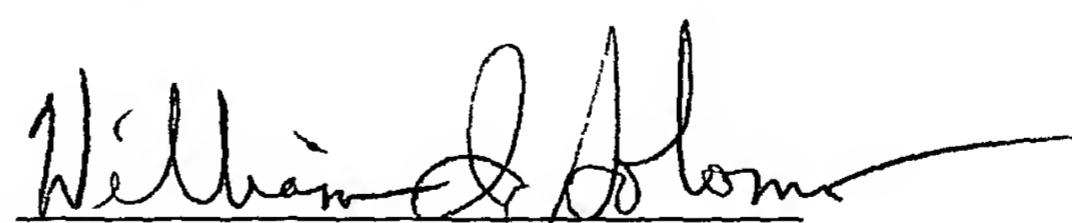
June 18, 2002

Sir:

In response to the Notice to File Missing Parts of Nonprovisional Application dated April 8, 2002, in connection with the above-identified application, attached hereto is an executed Declaration for Patent Application in compliance with 37 CFR 1.63. In addition, a payment in the amount of \$130.00 to cover the required surcharge for filing the declaration is also attached, in accordance with 37 CFR 1.16.

Please charge any shortages in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (501.41263X00) and please credit any excess fees to such deposit account.

Respectfully submitted,



William I. Solomon
Registration No. 28,565
ANTONELLI, TERRY, STOUT & KRAUS, LLP

WIS/gfa
Attachment(s)
(703) 312-6600

FF3



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/091,489	03/07/2002	Shinichi Nakabayashi	501.41263X00

CONFIRMATION NO. 9472

FORMALITIES LETTER



OC00000007825387

020457
ANTONELLI TERRY STOUT AND KRAUS
SUITE 1800
1300 NORTH SEVENTEENTH STREET
ARLINGTON, VA 22209

Date Mailed: 04/08/2002

NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

FILED UNDER 37 CFR 1.53(b)

Filing Date Granted

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is missing.
A properly signed oath or declaration in compliance with 37 CFR 1.63, identifying the application by the above Application Number and Filing Date, is required.
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(l) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.
- **The balance due by applicant is \$ 130.**

*A copy of this notice **MUST** be returned with the reply.*

[Signature]
Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

06/24/2002 MBERHE 00000007 10091489

01 FC:105

130.00 0P

300001469 - 06/26/02 MPW

501.41263X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): NAKABAYASHI, et al

Serial No.: 10/091,489

Filed: March 7, 2002

For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE

Group: 2812

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
Washington, D.C. 20231

June 18, 2002

Sir:

In the matter of the above-identified application,
applicant (s) hereby respectfully petition for an extension of
time to permit filing of a response within the one month
subsequent to the shortened statutory period set in the Notice
to File Missing Parts of Nonprovisional Application mailed
April 8, 2002. A payment in the amount of \$110.00 to cover
the required fee for the requested extension of time is
attached hereto.

It is respectfully requested that any shortage in the fee
be charged to the to Deposit Account No. 01-2135
(501.41263X00) and please credit any excess fees to such
deposit account.

Respectfully submitted,


William I. Solomon

Registration No. 28,565
ANTONELLI, TERRY, STOUT & KRAUS, LLP

WIS/gfa
Attachment(s)
(703) 312-6600